

# COMPLEXITY OF EQUIVALENCE RELATIONS AND PREORDERS FROM COMPUTABILITY THEORY

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**ABSTRACT.** We study the relative complexity of equivalence relations and preorders from computability theory and complexity theory. Given binary relations  $R, S$ , a componentwise reducibility is defined by

$$R \leq S \iff \exists f \forall x, y [xRy \leftrightarrow f(x)Sf(y)].$$

Here  $f$  is taken from a suitable class of effective functions. For us the relations will be on natural numbers, and  $f$  must be computable. We show that there is a  $\Pi_1^0$ -complete equivalence relation, but no  $\Pi_k^0$ -complete for  $k \geq 2$ . We show that  $\Sigma_k^0$  preorders arising naturally in the above-mentioned areas are  $\Sigma_k^0$ -complete. This includes polynomial time  $m$ -reducibility on exponential time sets, which is  $\Sigma_2^0$ , almost inclusion on r.e. sets, which is  $\Sigma_3^0$ , and Turing reducibility on r.e. sets, which is  $\Sigma_4^0$ .

## 1. INTRODUCTION

Mathematicians devote much of their energy to the classification of mathematical structures. In this endeavor, the tool most commonly used is the notion of a *reduction*: a map reducing a seemingly more complicated question to a simpler question. As a simple example, one classifies the countable rational vector spaces (under the equivalence relation of isomorphism) using the notion of dimension, by proving that two such vector spaces are isomorphic if and only if they have the same dimension over  $\mathbb{Q}$ . More generally, we have the following definition.

**Definition 1.1.** If  $E$  and  $F$  are equivalence relations on the domains  $D_E$  and  $D_F$ , a *reduction* from  $E$  to  $F$  is a function  $f : D_E \rightarrow D_F$  with the property that, for all  $x, y \in D_E$ ,

$$x E y \iff f(x) F f(y).$$

In our example,  $E$  is the isomorphism relation on the class  $D_E$  of countable vector spaces (with domain  $\omega$ , say, to avoid set-theoretical issues), and  $F$  is the equality relation on the set  $\{0, 1, 2, \dots, \omega\}$  of possible dimensions of these spaces.

A reduction  $f$  of a complicated equivalence relation  $E$  to a simpler equivalence relation  $F$  is entirely possible if one allows it to be sufficiently complex. (For instance, every equivalence relation  $E$  with exactly  $n$  classes can be

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reduced to the equality relation on  $\{1, 2, \dots, n\}$ , but this reduction has the same complexity as  $E$ .) Normally, however, the goal is for  $f$  be a readily understandable function, so that we can actually learn something from the reduction. In our example, one should ask how hard it is to compute the dimension of a rational vector space. It is natural to restrict the question to computable vector spaces (i.e. those where the vector addition is given as a Turing-computable function on the domain  $\omega$  of the space). Yet even when its domain  $D_E$  contains only the computable rational vector spaces, computing the function which maps each one to its dimension requires a  $\mathbf{0}'''$ -oracle, hence is not as simple as one might have hoped.

**Effective reductions.** Reductions are normally ranked by the ease of computing them. In the context of Borel theory, for instance, a large body of research is devoted to the study of *Borel reductions*. Here the domains  $D_E$  and  $D_F$  are the set  $2^\omega$  or some other standard Borel space, and a Borel reduction  $f$  is a reduction (from  $E$  to  $F$ , these being equivalence relations on  $2^\omega$ ) which, viewed as a function from  $2^\omega$  to  $2^\omega$ , is Borel. If such a reduction exists, one says that  $E$  is *Borel reducible to  $F$* , and writes  $E \leq_B F$ . A stronger possible requirement is that  $f$  be continuous, in which case we have (of course) a *continuous reduction*. In case the reduction is given by a Turing functional from reals to reals, it is a (*type-2*) *computable reduction*.

A further body of research is devoted to the study of the same question for equivalence relations  $E$  and  $F$  on  $\omega$ , and reductions  $f : \omega \rightarrow \omega$  between them which are computable. If such a reduction from  $E$  to  $F$  exists, we say that  $E$  is *computably reducible to  $F$* , and write  $E \leq_c F$ , or often just  $E \leq F$ . These reductions will be the focus of this paper.

The main purpose of this paper is to investigate the complexity of equivalence relations under these reducibilities. In certain cases we will generalize from equivalence relations to preorders on  $\omega$ . We restrict most of our discussion to relatively low levels of the hierarchy, usually to  $\Pi_n^0$  and  $\Sigma_n^0$  with  $n \leq 4$ . One can focus more closely on very low levels: such articles as [3, 13, 5], for instance, have dealt exclusively with  $\Sigma_1^0$  equivalence relations. At the other extreme, the papers [10, 9] consider equivalence relations outside the arithmetical or even hyperarithmetical, obtaining certain results about  $\Sigma_1^1$ -completeness of various equivalence relations on  $\omega$ . The work [8] is focused on much the same levels of the arithmetical hierarchy as our work here, but through the prism of Borel equivalence relations on  $2^\omega$ , by restricting such relations to the class of computably enumerable subsets of  $\omega$ . A few of their results will be extended here. Finally, the complexity of equivalence relations under this kind of reducibility has recently been studied by a number of authors from various disciplines. Within complexity theory it seems to have been first introduced in [12], and later studied in [6].

**Completeness.** Equivalence relations on  $\omega$  are subsets of  $\omega^2$ , and so those with arithmetical definitions can be ranked in the usual hierarchy as  $\Sigma_n^0$  and/or  $\Pi_n^0$  for various  $n \in \omega$ . The question of *completeness* then naturally arises. A  $\Sigma_n^0$  equivalence relation  $F$  is *complete* (among equivalence relations under  $\leq_c$ ) if for every  $\Sigma_n^0$  equivalence relation  $E$ , we have  $E \leq_c F$ .  $\Pi_n^0$ -completeness is defined similarly. Much of this paper is devoted to the study

of complete equivalence relations at various levels of the arithmetical hierarchy, and we will often speak of their  $\Sigma_n^0$ -completeness or  $\Pi_n^0$ -completeness without specifying the reducibility  $\leq_c$ . At certain times we will need to consider other reducibilities on subsets of  $\omega$ , such as Turing reducibility  $\leq_T$  or 1-reducibility  $\leq_1$ , and these reducibilities give rise to their own (analogous) notions of completeness, but when referring to those notions we will always name them specifically.

The standard notion of an  $m$ -reduction from one set to another is relevant here, since computable reducibility on equivalence relations can be seen as one of two natural ways to extend  $m$ -reducibility from sets to binary relations. However when comparing the complexity between equivalence relations, it is more natural to use computable reducibility. Following Definition 1.1, we define:

$$(1) \quad A \leq_c B \iff \exists \text{ computable } g \forall x, y [\langle x, y \rangle \in A \leftrightarrow \langle g(x), g(y) \rangle \in B].$$

The resulting reducibility has many interesting and, at times, surprising properties. It can be readily seen that (1) is a stronger property than  $m$ -reducibility:  $A \leq_c B$  via  $g$  can be seen to imply  $A \leq_m B$  via  $f$  by letting  $f(x, y) = \langle g(x), g(y) \rangle$ , so a relation complete for a level of the arithmetic hierarchy in the sense of relations (1) is also complete in the sense of sets. The converse fails: in Subsection 3.2 we shall see that some levels do not have complete equivalence relations at all.

It is readily seen that, for every  $n$ , there exists an equivalence relation that is  $\Sigma_n^0$ -complete under  $\leq_c$ . In Section 3 we demonstrate the existence of  $\Pi_1^0$ -complete equivalence relations, which is not nearly as obvious. We give a natural example from complexity, namely equality of quadratic time computable functions. In contrast, we also show that for  $n \geq 2$ , no equivalence relation is  $\Pi_n^0$ -complete.

**Preorders.** Equivalence relations were the first context in which reductions arose, but the notion of a reduction can be applied equally well to any pair of binary relations: the  $E$  and  $F$  in Definition 1.1 need not be required to be equivalence relations. In fact, a reduction could be considered as a homomorphism of structures in the language containing a single binary relation symbol, whether or not that symbol defines an equivalence relation on either structure. Preorders (i.e. reflexive transitive binary relations  $\preceq$ , with  $x \preceq y \preceq x$  allowed even when  $x \neq y$ ) form a natural collection of binary relations on which to study reductions, and in Section 4 we will turn to the question of computable reducibility on preorders, with some interesting uses of effectively inseparable Boolean algebras. In this way we obtain some examples of natural  $\Sigma_n^0$ -complete preorders from computability theory. For instance, almost inclusion  $\subseteq^*$  of r.e. sets, and weak truth table reducibility on r.e. sets, are  $\Sigma_3^0$  complete preorders. To obtain corresponding completeness results for equivalence relations, we use the easy Fact 2.1 that completeness of a preorder  $P$  at some level of the arithmetical hierarchy implies completeness of the corresponding symmetric fragment which is an equivalence relation. Using this we obtain, for instance, the  $\Sigma_3^0$  completeness of several of the equivalence relations considered in [8]. In Section 5 we show that Turing reducibility on r.e. sets is a  $\Sigma_4^0$ -complete preorder, and hence

Turing equivalence on r.e. sets is also  $\Sigma_4^0$ -complete as an equivalence relation. Using a direct construction, Fokina, Friedman and Nies [11] have proved that 1-equivalence and  $m$ -equivalence on r.e. sets are  $\Sigma_3^0$ -complete.

Our results can be applied to obtain completeness results for preorders and equivalence relations from effective algebra. For instance, Ianovski [14] has proved the  $\Sigma_3^0$ -completeness of effective embeddability for computable subgroups of the additive group of rationals by reducing almost inclusion of r.e. sets. On the other hand, the completeness of several natural equivalence relations, such as isomorphism of finitely presented groups, remains open. Such questions are discussed in the final section.

The articles already cited here are among the principal references for this subject. For ordinary computability-theoretic questions, we suggest [23], a second edition of which is to appear soon.

## 2. PRELIMINARIES

Let  $X^{[2]}$  denote the unordered pairs of elements of  $X$ . A set  $R \subseteq X^{[2]}$  is *transitive* if  $\{u, v\}, \{v, w\} \in R$  and  $u \neq w$  implies  $\{u, w\} \in R$ . We view equivalence relations as transitive subsets of  $\omega^{[2]}$ .

**Fact 2.1.** *Let  $P$  be a preorder,  $E = \{\{x, y\} : \langle x, y \rangle \in P \text{ and } \langle y, x \rangle \in P\}$ , and  $n \in \omega$ . If  $P$  is complete among  $\Sigma_n^0 [\Pi_n^0]$  preorders under  $\leq_c$  then  $E$  is complete among  $\Sigma_n^0 [\Pi_n^0]$  equivalence relations under  $\leq_c$ .*

*Proof.* We give the proof for  $\Sigma_n^0$ . Note that  $E$  is clearly  $\Sigma_n^0$ . Let  $F$  be an arbitrary  $\Sigma_n^0$  equivalence relation. In particular  $F$  can be seen as a preorder and so there is a computable function  $f$  such that for every  $x, y$ ,  $\{x, y\} \in F$  iff  $\langle f(x), f(y) \rangle \in P$ . Then  $f$  witnesses that  $F \leq_c E$ .  $\square$

In parts of the Subsections 3.1 and 4.3 we will assume familiarity with the basic notions of computational complexity theory; see for instance [4]. We consider languages  $A \subseteq \Sigma^*$  for the alphabet  $\Sigma = \{0, 1\}$ . Recall that for a function  $h : \mathbb{N} \rightarrow \mathbb{N}$ , a language  $A$  is *computable in time  $h$*  if there is an  $O(h)$  time-bounded multitape Turing machine (TM) deciding membership in  $A$ . The class of such languages is denoted  $\text{DTIME}(h)$ . Languages in  $\text{DTIME}(n)$  are also called *linear time*.

Recall that a function  $h$  from numbers to strings with  $h(n) \geq n$  is *time constructible* if  $h(n)$  can be computed in  $O(h(n))$  steps on a multitape TM. Suppose  $c \in \mathbb{N}$  is a constant. If a function  $h : \mathbb{N} \rightarrow \mathbb{N}$  is time constructible, then we can equip any given Turing machine with a “clock” on an extra tape. This is a counter initialized at  $c \cdot h(n)$  and decremented each step of the given machine. It stops the given machine on reaching 0. Since  $h$  is time constructible, the number of steps the clocked machine needs is still within  $O(h)$ . Numbering all Turing machines clocked in this way yields an effective listing of the class  $\text{DTIME}(h)$ .

## 3. $\Pi_n^0$ -COMPLETENESS FOR EQUIVALENCE RELATIONS

In this section we show that there is a  $\Pi_1^0$ -complete equivalence relation, but no  $\Pi_n^0$ -complete equivalence relation for  $n \geq 2$ .

**3.1.  $\Pi_1^0$  equivalence relations.** For each function  $f: \mathbb{N} \times \mathbb{N} \rightarrow \mathbb{N}$  and  $x \in \mathbb{N}$ , let  $f_x$  denote the function  $n \mapsto f(x, n)$ . If  $f$  is computable,

$$E_f = \{\langle x, y \rangle : f_x = f_y\}$$

appears to be a natural example of a  $\Pi_1^0$  equivalence relation. We show that indeed every  $\Pi_1^0$  equivalence relation is obtained in this way. A slight extension of the argument yields a function  $f$  such that the corresponding equivalence relation  $E_f$  is  $\Pi_1^0$ -complete. We show that  $f$  can in fact be chosen polynomial time computable.

For each  $\Pi_1^0$  equivalence relation  $E$ , there is a computable sequence of cofinite relations  $(E_t)_{t \in \mathbb{N}}$  contained in  $\mathbb{N}^{[2]}$  with  $E = \bigcap_t E_t$  such that from  $t$  we can compute a strong index for the complement of  $E_t$ , we have  $E_t \supseteq E_{t+1}$  and

$$(2) \quad E_t \cap [0, t]^{[2]} \text{ is transitive.}$$

**Proposition 3.1.** *For each  $\Pi_1^0$  equivalence relation  $E$ , there is a computable binary function  $f$  such that  $E = E_f$ , namely, for each  $x, y$ , we have*

$$(3) \quad Exy \leftrightarrow f_x = f_y.$$

*Proof.* Let  $(E_t)_{t \in \mathbb{N}}$  be as in (2). We define  $f(x, n)$  by recursion on  $x$ . Let

$$f(x, n) = \begin{cases} f(r, n), & \text{if } r < x \text{ for the least } r \text{ such that } E_{\max(x, n)}rx, \\ x, & \text{otherwise.} \end{cases}$$

Note that for each  $z$ , for sufficiently large  $k$ , we have  $f(z, k) = \min[z]_E$ . We verify that (3) is satisfied.

For the implication “ $\rightarrow$ ”, we show the following by induction on  $x$ .

**Claim.** *For each  $y < x$ , for each  $n$ , if  $E_{\max(x, n)}yx$  then  $f_y(n) = f_x(n)$ .*

To see this, let  $r < x$  be as in the definition of  $f(x, n)$ . Then  $r \leq y < x$ . By definition of  $f$  we have  $f_x(n) = f_r(n)$ . By transitivity in (2), we have  $E_{\max(x, n)}ry$ , and hence  $E_{\max(y, n)}ry$ . Then by inductive hypothesis,  $f_r(n) = f_y(n)$ .

For the implication “ $\leftarrow$ ”, suppose that  $\neg Eyx$ . Then  $f_y(k) \neq f_x(k)$  for sufficiently large  $k$  by the remark after the definition of  $f$  above.  $\square$

As a corollary we obtain a presentation of  $\Pi_1^0$  equivalence relations as the uniform intersection of recursive ones.

**Corollary 3.2.** *For each  $\Pi_1^0$  equivalence relation  $E$ , there is a uniformly recursive sequence  $(F_n)_{n \in \mathbb{N}}$  of equivalence relations such that  $F_n \supseteq F_{n+1}$  and  $E = \bigcap_n F_n$ .*

*Proof.* Let  $E = E_f$  for a computable function  $f$ . Let

$$F_n = \{\langle x, y \rangle : f_x \upharpoonright_n = f_y \upharpoonright_n\},$$

where  $g \upharpoonright_n$  denotes the tuple  $\langle g(0), \dots, g(n-1) \rangle$ .  $\square$

We obtain a  $\Pi_1^0$ -complete equivalence relation by exposing a uniformity in the proof of Proposition 3.1.

**Theorem 3.3.** *There is a computable binary function  $g$  such that the equivalence relation  $E_g = \{\langle x, y \rangle : g_x = g_y\}$  is  $\Pi_1^0$ -complete.*

*Proof.* Let  $E^i = \omega^{[2]} - W_i$  where the  $i$ -th r.e. set  $W_i$  is viewed as a subset of  $\omega^{[2]}$ . Uniformly in  $i$  we may obtain a computable sequence of strong indices  $(E_t^i)_{t \in \mathbb{N}}$  as in the proof of Prop. 3.1 such that  $E^i = \bigcap_t E_t^i$  whenever  $E^i$  is transitive. Now define  $g(\langle i, x \rangle, n)$  to have the value  $f(x, n)$  as above where  $E = E^i$ . By the argument in that proof, if  $E^i$  is transitive then  $E^i \leq_c E_g$  via the function  $x \mapsto \langle i, x \rangle$ .  $\square$

We can in fact obtain in Theorem 3.3 a function on binary strings that is quadratic time computable (see Section 2). We thank Moritz Müller at the Kurt Gödel Research Institute in Vienna for suggesting a simplified proof which we provide at the end of this subsection.

**Lemma 3.4.** *For each computable binary function  $g$ , there is a quadratic time computable binary function  $G$  defined on strings over  $\{0, 1\}$ , and a computable unary function  $p$  mapping numbers to strings, such that  $g(x, n) = G(p(x), p(n))$ , and  $G(w, v) = 0$  for any string  $v$  not of the form  $p(n)$ .*

Unary quadratic time functions over the alphabet  $\{0, 1\}$  are given by indices for multitape Turing machines (TM) that are equipped with a counter forcing them to stop in time  $O(n^2)$ , where  $n$  is the input length.

**Theorem 3.5.** *(i) There is a quadratic time computable binary function  $G$  such that  $E_G$  is  $\Pi_1^0$ -complete. (ii) Equality of unary quadratic time computable functions is a  $\Pi_1^0$ -complete equivalence relation.*

*Proof of Theorem.* (i) Let  $G$  be the function obtained from the function  $g$  of Theorem 3.3 according to the lemma. Let  $p$  be as in the lemma. Then  $E_g xy \leftrightarrow G_{p(x)} = G_{p(y)}$ . Since  $E_g$  is complete, so is  $E_G$ .

(ii) Since all the Turing machines considered stop on every input in a quadratic time bound, the equivalence relation is  $\Pi_1^0$ . The completeness follows from (i) since from a string  $w$  we can compute an index for a quadratic time TM computing  $G_w$ .  $\square$

*Proof of Lemma 3.4.* Let  $g(x, y)$  (with  $x, y$  represented in unary) be computable in time  $t(x, y)$  where  $t$  is a computable function that is increasing in both  $x$  and  $y$ . We want a time constructible  $h$  satisfying  $t(n, k) \leq h(n) + h(k)$ . This can be achieved by having  $h(n)$  run  $g(n, n)$  and counting the number of steps. This satisfies  $t(n, k) \leq h(n) + h(k)$  as for  $n \leq k$ ,  $t(n, k) \leq t(k, k) = h(k)$ , and  $h(n) = m$  can be calculated in time quadratic in  $m$ : place a binary counter at the start of the tape and simulate  $g$  to the right of it. In the worst case scenario for every step of  $f$  the machine would iterate over  $m + \log m$  cells, and whenever the counter grows in size the entire tape would need to be shifted right which in total would take  $m \log m$ , coming to  $m \log m + m(m + \log m)$  or  $O(m^2)$ .

To compute  $G(a, b)$ , verify whether  $a = 1^x 0 1^{h(x)}$  and  $b = 1^n 0 1^{h(n)}$ . If so output  $g(x, n)$ , else output 0. Observe that  $G$  is quadratic time: we can verify whether  $a = 1^x 0 1^z$  is of the form  $1^x 0 1^{h(x)}$  by beginning to compute  $g(x, x)$ , stopping whenever the number of steps exceeds  $z$ . If the input is of the right form we can compute  $g(x, n)$  in time  $t(x, n) \leq h(x) + h(n) = O(|a| + |b|)$ .

Note that for every  $x, n$ ,

$$G(1^x 0 1^{h(x)}, 1^n 0 1^{h(n)}) = g(x, n),$$

and for all other values the function is 0. Thus the function  $p(y) = 1^y 0 1^{h(y)}$  is as required.  $\square$

In work independent of ours [7, Proposition 3.1] the authors offer a characterisation of  $\Pi_1^0$  preorders: every such preorder is computably isomorphic to the inclusion relation on a uniformly computable family of sets. As an equivalence relation is merely a symmetric preorder, a  $\Pi_1^0$  equivalence relation is then isomorphic to equality on such a family. Since sets can be viewed as 0/1 valued functions, this yields another proof of Prop. 3.1. Methods similar to the ones employed in the proof of Theorem 3.5 above now yield a version of that Theorem for the inclusion of quadratic time computable languages.

**Theorem 3.6** ([14] based on [7]). *Inclusion of quadratic time computable languages is a complete  $\Pi_1^0$  preorder.*

For a proof see [14, Thm 5.5]. Using their result one can also obtain a stronger form of Cor. 3.2 where the  $F_n$  have at most  $2^n$  equivalence classes.

**3.2. For  $n \geq 2$  there is no  $\Pi_n^0$ -complete equivalence relation.**  $\Pi_n^0$  equivalence relations often occur naturally. For instance, equality of r.e. sets as a relation on indices is  $\Pi_2^0$ . See [8] for further examples along these lines. Computable model theory is a further source of interesting examples. Consider the class  $\mathcal{P}$  of computable permutations on  $\omega$  with all cycles finite. Isomorphism as a relation on computable indices for structures in this class is  $\Pi_2^0$ . This equivalence relation is easily seen to be computably equivalent to equality on r.e. sets. Melnikov and Nies have studied isometry of compact computable metric spaces and shown that it is properly  $\Pi_3^0$ ; for effectively compact spaces it is properly  $\Pi_2^0$ .

Here we show that there is no  $\Pi_n^0$ -complete equivalence relation for  $n \geq 2$ . Intuitively speaking, one would then expect that the computable reducibility degrees of  $\Pi_n^0$  equivalence relations are complicated.

**Theorem 3.7.** *For each  $\Pi_2^0$  equivalence relation  $E$  there is a  $\Delta_2^0$  equivalence relation  $L$  such that  $L \not\leq_c E$ .*

Relativizing to  $\emptyset^{(n)}$  yields an immediate corollary.

**Corollary 3.8.** *For every  $n \in \omega$ , no  $\Pi_{n+2}^0$  equivalence relation can be hard for all the  $\Delta_{n+2}^0$  equivalence relations under  $\mathbf{0}^{(n)}$ -computable reducibility  $\leq_{\mathbf{0}^{(n)}}$ , let alone under computable reducibility  $\leq_c$ .*  $\square$

This can in fact be extended to  $\mathbf{0}^{(\alpha)}$  where  $\alpha$  is a computable ordinal. In contrast, there is a  $\Pi_1^1$ -complete equivalence relation, because the class of symmetric  $\Pi_1^1$  relations is closed under taking the transitive closure.

*Proof of Theorem 3.7.*  $E$  be any  $\Pi_2^0$  equivalence relation on  $\omega$ . Since  $E$  is  $\Pi_1^{0,\emptyset'}$  we may fix a  $\emptyset'$ -computable function  $f$  such that  $\langle x, y \rangle \in E$  iff  $f(x, y, t) = 1$  for every  $t$ . Now define  $L$  as follows.

For  $e_0 \neq e_1$ ,  $\{\langle e_0, s_0 \rangle, \langle e_1, s_1 \rangle\} \notin L$  for any  $s_0, s_1$ . For each  $e$ , we distinguish two cases.

*Case 1.* There is a number  $t$ , chosen least, such that



$\varphi_e(\langle e, 0 \rangle) \downarrow = x$ ,  $\varphi_e(\langle e, 1 \rangle) \downarrow = y$ ,  $\varphi_e(\langle e, t+2 \rangle) \downarrow = z$ , and  $f(x, y, t) = 0$ .

Let  $v$  be least such that  $f(x, z, v) = 0$  or  $f(y, z, v) = 0$ .

- If  $f(x, z, v) = 0$  we set  $\{\langle e, 0 \rangle, \langle e, t+2 \rangle\} \in L$ .
- If  $f(y, z, v) = 0$  we set  $\{\langle e, 1 \rangle, \langle e, t+2 \rangle\} \in L$ .

Declare  $\{\langle e, s_0 \rangle, \langle e, s_1 \rangle\} \notin L$  for every other pair  $s_0 \neq s_1$ .

*Case 2.* There is no such number  $t$ . Then we set  $\{\langle e, s_0 \rangle, \langle e, s_1 \rangle\} \notin L$  for every  $s_0 \neq s_1$ .

To see that  $L$  is  $\Delta_2^0$ , it suffices to note that for each  $e$ , if Case 1 applies then by the transitivity of  $E$  the number  $v$  exists. Transitivity of  $L$  follows from the fact that we relate each element to at most one other element.

Now suppose that  $L \leq_c E$  via the total function  $\varphi_e$ . Since  $\{\langle e, 0 \rangle, \langle e, 1 \rangle\} \notin L$  it follows that the least number  $t$  described above exists. By construction we ensure that either  $\{\langle e, 0 \rangle, \langle e, t+2 \rangle\} \in L$  and  $\{x, z\} \notin E$ , or we have  $\{\langle e, 1 \rangle, \langle e, t+2 \rangle\} \in L$  and  $\{y, z\} \notin E$ , a contradiction.  $\square$

**Discussion.** Observe that given any two  $\Pi_{n+2}^0$  equivalence relations  $R, S$ , the disjoint union  $R \sqcup S = \{\{2n, 2m\} \mid nRm\} \cup \{\{2n+1, 2m+1\} \mid nSm\}$  is also a  $\Pi_{n+2}^0$  equivalence relation and we have  $R, S \leq_c R \sqcup S$ . Hence the proof of Theorem 3.7 shows that amongst  $\Pi_{n+2}^0$  equivalence relations there can be no maximal element under  $\leq_c$ .

The following is an immediate consequence of Theorem 3.7; for preorders we also apply Fact 2.1.

**Corollary 3.9.** *For each  $n \geq 2$ , there is no  $\Pi_n^0$ -complete preorder, and no  $\Delta_n^0$ -complete equivalence relation, or preorder.*

There also is no  $\Pi_2^0$  analog of Cor. 3.2. Let us say a  $\Pi_2^0$  equivalence relation  $E$  is *effectively*  $\Pi_2^0$  if there is a uniformly r.e. sequence  $(F_n)_{n \in \mathbb{N}}$  of equivalence relations such that  $E = \bigcap_n F_n$ . (We may also require  $F_n \supseteq F_{n+1}$  without loss of generality.) If  $S$  is an r.e. nonrecursive set then the  $\Delta_2^0$  equivalence relation with the two classes  $S$  and  $\omega - S$  is not effectively  $\Pi_2^0$ . Note that we can effectively list the effectively  $\Pi_2^0$  equivalence relations, so that there is a complete one under computable reducibility.

#### 4. $\Sigma_k^0$ -COMPLETE PREORDERS

The concept of an effectively inseparable r.e. Boolean algebra was introduced by Kripke and Pour-El [22, Lemmas 1,2] when they studied the complexity of logical derivability for recursively axiomatizable theories. The method was developed in more generality by Montagna and Sorbi [18]. For instance, in their Theorem 3.1 they showed that the preorder associated with any effectively inseparable Boolean algebra is  $\Sigma_1^0$ -complete. They developed their method further in order to show that for a sufficiently strong arithmetical theory  $T$  (such as an axiomatizable extensions of PA), for any r.e. preorder  $\preceq$  there is a  $\Sigma_1$  formula  $G$  with  $i \preceq j$  iff  $T \vdash G(i) \rightarrow G(j)$ .

We are interested mostly in semantic preorders such as reducibilities, which are usually “higher up” in the arithmetical hierarchy. So we extend the methods discussed above in order to show the completeness of a  $\Sigma_k^0$  preorder, using effectively inseparable  $\Sigma_k^0$  Boolean algebras. Thereafter we apply it to preorders in the subrecursive setting, which are  $\Sigma_2^0$ , and to preorders on



the r.e. sets arising from computability theory, which are  $\Sigma_3^0$ . Each time, we naturally embed an effectively inseparable  $\Sigma_k^0$  Boolean algebra for the appropriate  $k$  into the preorder.

For sets  $X, Y$ , we write  $X \subseteq^* Y$  ( $X$  is almost contained in  $Y$ ) if  $X \setminus Y$  is finite. We write  $X =^* Y$  if  $X \subseteq^* Y \subseteq^* X$ . The preorders we will consider in this section for the cases  $k = 2, 3$  are given on certain natural classes of sets, either by almost containment, or by reducibilities.

**4.1. The method of effectively inseparable  $\Sigma_k^0$  Boolean algebras.** Our proofs rest on the following notion, which is an analog of creativity for disjoint pairs of sets. For detail see [23, II.4.13]. We relativize r.e. sets to  $\emptyset^{(k-1)}$ , thereby obtaining  $\Sigma_k^0$  sets. Note, however, that the analog  $g$  of the productive function remains computable.

**Definition 4.1.** We say that disjoint  $\Sigma_k^0$  sets  $A, B \subseteq \mathbb{N}$  are *effectively inseparable* (e.i.) if there is a computable map  $g$  (called a productive function) such that for each  $p, q \in \mathbb{N}$ ,

$$\begin{aligned} &\text{if } A \subseteq W_p^{\emptyset^{(k-1)}}, B \subseteq W_q^{\emptyset^{(k-1)}} \text{ and } W_p^{\emptyset^{(k-1)}} \cap W_q^{\emptyset^{(k-1)}} = \emptyset, \\ &\text{then } g(p, q) \notin W_p^{\emptyset^{(k-1)}} \cup W_q^{\emptyset^{(k-1)}}. \end{aligned}$$

**Remark 4.2.** If we enlarge both components of the disjoint pair, the function  $g$  is still a productive function.

We say that a disjoint pair  $A, B$  is of  $\Sigma_k^0$  sets *m-complete for disjoint pairs* if for each pair  $U, V$  there is a computable map  $f$  such that  $x \in U \leftrightarrow f(x) \in A$ , and  $x \in V \leftrightarrow f(x) \in B$ . It is well-known [23, II.4.13] that each e.i. pair of  $\Sigma_k^0$  sets is *m-complete* in this sense. Furthermore, the *m-reduction* can be determined from indices of the four given  $\Sigma_k^0$  sets, and the productive function.

We consider Boolean algebras in the language with partial order  $\leq$ , meet  $\wedge$ , join  $\vee$ , and complementation  $'$ . A  $\Sigma_k^0$  Boolean algebra  $\mathcal{B}$  is represented by a model  $(\mathbb{N}, \preceq, \vee, \wedge, ')$  such that  $\preceq$  is a  $\Sigma_k^0$  preorder,  $\vee, \wedge$  are computable binary functions,  $'$  is a computable unary function, and the quotient structure  $(\mathbb{N}, \preceq, \vee, \wedge, ')/\approx$  is isomorphic to  $\mathcal{B}$ . Here  $\approx$  is the equivalence relation corresponding to  $\preceq$ , and we assume the functions are compatible with  $\approx$ . We may also assume that  $0 \in \mathbb{N}$  denotes the least element of the Boolean algebra, and  $1 \in \mathbb{N}$  denotes the greatest element. If necessary, we write  $\preceq_{\mathcal{B}}$  etc. to indicate the Boolean algebra a relation on  $\mathbb{N}$  belongs to.

The usual effectiveness notions defined for sets or functions on the natural numbers can be transferred to  $\mathcal{B}$ . For instance, we say that an ideal  $I$  of  $\mathcal{B}$  is  $\Sigma_k^0$  if  $I$  is  $\Sigma_k^0$  when viewed as a subset of  $\mathbb{N}$ .

Let  $\mathcal{F}$  be a computable Boolean algebra that is freely generated by a computable sequence  $(p_n)_{n \in \mathbb{N}}$ . For instance, we can take as  $\mathcal{F}$  the finite unions of intervals  $[x, y)$  in  $[0, 1)_{\mathbb{Q}}$ . We fix an effective encoding of  $\mathcal{F}$  by natural numbers. Then, equivalent to the definition above, a  $\Sigma_k^0$  Boolean algebra is given in the form  $\mathcal{F}/\mathcal{I}$ , where  $\mathcal{I}$  is a  $\Sigma_k^0$  ideal of  $\mathcal{F}$ . We rely on this view for coding a  $\Sigma_k^0$  preorder into a  $\Sigma_k^0$  Boolean algebra. We slightly extend concepts and results of [18] where  $k = 1$ .

**Lemma 4.3.** *For any  $\Sigma_k^0$  preorder  $\preceq$ , there is a  $\Sigma_k^0$  ideal  $\mathcal{I}$  of  $\mathcal{F}$  such that  $n \preceq k \Leftrightarrow p_n - p_k \in \mathcal{I}$ .*

*Proof.* Let  $\mathcal{I}$  be the ideal of  $\mathcal{F}$  generated by  $\{p_n - p_k : n \preceq k\}$ . The implication “ $\Rightarrow$ ” follows from the definition. For the implication “ $\Leftarrow$ ”, let  $\mathcal{B}_{\preceq}$  be the Boolean algebra generated by the subsets of  $\omega$  of the form  $\hat{i} = \{r : r \preceq i\}$ . The map  $p_i \mapsto \hat{i}$  extends to a Boolean algebra homomorphism  $g : \mathcal{F} \rightarrow \mathcal{B}_{\preceq}$  that sends  $\mathcal{I}$  to 0. If  $n \not\preceq k$  then  $\hat{n} \not\subseteq \hat{k}$ , and hence  $p_n - p_k \notin \mathcal{I}$ .  $\square$

**Definition 4.4.** We say that a  $\Sigma_k^0$  Boolean algebra  $\mathcal{B}$  is  $\Sigma_k^0$  *effectively inseparable* (e.i.) if the sets  $[0]_{\approx}$  and  $[1]_{\approx}$  (that is, the names for  $0 \in \mathcal{B}$  and for  $1 \in \mathcal{B}$ , respectively) are effectively inseparable.

We will frequently use the following criterion to show that a  $\Sigma_k^0$  Boolean algebra  $\mathcal{B}$  is effectively inseparable.

**Fact 4.5.** Suppose that  $U, V$  is a pair of e.i.  $\Sigma_k^0$  sets. Let  $\mathcal{B}$  be a  $\Sigma_k^0$  Boolean algebra. Suppose there is a computable function  $g$  such that

$$x \in U \rightarrow g(x) \approx 0 \text{ and } x \in V \rightarrow g(x) \approx 1.$$

Then  $\mathcal{B}$  is effectively inseparable.

*Proof.* Let  $A = \{x : g(x) \approx 0\}$  and  $B = \{x : g(x) \approx 1\}$ . Then  $A, B$  are  $\Sigma_k^0$  sets,  $A \cap B = \emptyset$ ,  $U \subseteq A$ , and  $V \subseteq B$ . By Remark 4.2 this implies that  $A, B$  are effectively inseparable as  $\Sigma_k^0$  sets, whence  $\mathcal{B}$  is effectively inseparable as a  $\Sigma_k^0$  Boolean algebra.  $\square$

We slightly extend a result [18, Prop. 3.1]. As mentioned already, that work goes back to results of Kripke and Pour-El [22, Lemmas 1,2] who only worked in the setting of axiomatizable theories. It is worth including a proof of the extension because we only partially relativize the setting of [18]: the preorder of the Boolean algebra is  $\Sigma_k^0$ , while all of its algebraic operations, as well as the reduction function in the definition of completeness, are still computable.

**Theorem 4.6.** Suppose a Boolean algebra  $\mathcal{B}$  is  $\Sigma_k^0$ -effectively inseparable. Then  $\preceq_{\mathcal{B}}$  is a  $\Sigma_k^0$ -complete preorder.

*Proof.* We say that a  $\Sigma_k^0$  Boolean algebra is  $\Sigma_k^0$ -complete if each  $\Sigma_k^0$  Boolean algebra  $\mathcal{C}$  is computably embedded into it. We first show that the given Boolean algebra  $\mathcal{B}$  is  $\Sigma_k^0$ -complete. From  $x, y \in \mathcal{B}$  we can uniformly obtain a computable map  $h$  such that  $h(p) \approx (p \vee x) \wedge y$ . Note that if  $x \prec y$  then the range of  $h$  is contained in  $[x, y]$ , and  $h(0) \approx x, h(1) \approx y$ . Thus the disjoint  $\Sigma_k^0$  sets  $\{p : h(p) \approx x\}$  and  $\{p : h(p) \approx y\}$  are also e.i.

Given a  $\Sigma_k^0$  Boolean algebra  $\mathcal{C}$ , this allows us to construct the desired computable embedding  $h$  of  $\mathcal{C}$  into  $\mathcal{B}$  in a stepwise fashion. Recall that the domain of a presentation of  $\mathcal{C}$  is  $\mathbb{N}$  by our definition. Suppose  $y_i = h(i)$  has been defined for  $i < n$ . For an element  $z$  of a Boolean algebra define  $z^{(0)} = z$  and  $z^{(1)} = z'$ . For each  $n$ -bit string  $\tau$  let

$$p_{\tau} = \bigwedge_{i < n} i^{(\tau_i)} \text{ and } y_{\tau} = \bigwedge_{i < n} y_i^{(\tau_i)}.$$

By Remark 4.2, from  $\tau$  we can effectively determine  $z_{\tau} \preceq_{\mathcal{B}} y_{\tau}$  such that

$$n \wedge p_{\tau} \approx_{\mathcal{C}} 0 \Rightarrow z_{\tau} \approx_{\mathcal{B}} 0, \text{ and } n \wedge p_{\tau} \approx_{\mathcal{C}} p_{\tau} \Rightarrow z_{\tau} \approx_{\mathcal{B}} y_{\tau}.$$

Let  $h(n) = \bigvee_{|\tau|=n} z_\tau$ . It is clear that the map  $h$  is a computable embedding as required.

Now let  $\preceq_P$  be any  $\Sigma_k^0$  preorder. By Lemma 4.3 there is a  $\Sigma_k^0$  ideal  $\mathcal{I}$  of the free Boolean algebra  $\mathcal{F}$  such that  $n \preceq k \Leftrightarrow p_n - p_k \in \mathcal{I}$ . Since  $\mathcal{B}$  is  $\Sigma_k^0$ -complete, there is a computable embedding  $g$  of  $\mathcal{F}/\mathcal{I}$  into  $\mathcal{B}$ . Thus,  $n \preceq_P k \Leftrightarrow g(p_n) \preceq_{\mathcal{B}} g(p_k)$ .  $\square$

A forth-and-back version of the argument above shows that any two e.i.  $\Sigma_k^0$  Boolean algebras are effectively isomorphic. (This fact for  $k = 1$  was already noted in [22, 18].) It is interesting in view of the applications below: for a fixed  $k$ , all the preorders considered are complete for the “same reason”.

**4.2. Derivability in first-order logic.** Let  $T$  be a recursively axiomatized sufficiently strong theory in the language of arithmetic, such as Robinson arithmetic  $Q$ . Note that such a theory  $T$  can be finitely axiomatizable. The following is due to Kripke and Pour-El [22], and [18, Section 4].

**Theorem 4.7.** *For sentences  $\phi, \psi$  in the language of  $T$ , let  $\phi \preceq \psi$  if  $\vdash_T \phi \rightarrow \psi$ . Then  $\preceq$  is a  $\Sigma_1^0$ -complete preorder.*

*Proof.* Clearly  $\preceq$  is  $\Sigma_1^0$ . By [22], or see [18, Section 4], the Lindenbaum algebra of  $T$  is  $\Sigma_1^0$ -effectively inseparable. Now Theorem 4.6 implies that  $\preceq$  is complete.  $\square$

In [14] the first author has given a direct proof that logical implication for the first order language in a full signature is  $\Sigma_1^0$ -complete. His proof relies on a coding of Turing machine computations.

**4.3.  $\Sigma_2^0$  preorders in the resource-bounded setting.** For relevant notation from complexity theory see Section 2. In particular there is an effective listing of the class  $\text{DTIME}(h)$ , which we fix below without further mention. We use a technical tool:

**Lemma 4.8.** *Given a disjoint pair of  $\Sigma_2^0$  sets  $U, V$ , we can effectively in  $x$  determine a linear time computable language  $L_x$  such that*

$$x \in U \rightarrow L_x \text{ is finite, and } x \in V \rightarrow L_x \text{ is cofinite.}$$

*Proof.* Fix a uniformly r.e. pair of sequences  $(S_x)_{x \in \mathbb{N}}, (T_x)_{x \in \mathbb{N}}$  such that

$$x \in U \rightarrow S_x \text{ is finite, and } x \in V \rightarrow T_x \text{ is finite.}$$

To define  $L_x$ , at stage  $n$ , in linear time we determine whether  $w \in L_x$  for each string  $w$  of length  $n$ . Let  $t \leq n$  be largest such that  $t > 0$  and in  $n$  steps one can verify that (a)  $S_{x,t} \neq S_{x,t-1}$  or (b)  $T_{x,t} \neq T_{x,t-1}$ ; if there is no such  $t$ , let  $t = 0$ . If (a) applies or  $t = 0$ , declare that  $w$  is in  $L_x$ . Otherwise, declare that  $w$  is not in  $L_x$ .

Clearly  $L_x$  is computable in linear time, uniformly in  $x$ . If  $x \in U$ , then  $S_x$  is finite, so  $T_x$  is infinite. Then for almost all  $n$  we are in case (b), so  $L_x$  is finite. If  $x \in V$  then  $T_x$  is finite, so  $S_x$  is infinite, so for almost all  $n$  we are in case (a) and  $L_x$  is cofinite.  $\square$

We obtain a variation on Theorem 3.6.

**Theorem 4.9.** *The preorder of almost inclusion  $\subseteq^*$  among quadratic time computable languages is  $\Sigma_2^0$ -complete.*

*Proof.* Let  $(G_i)_{i \in \mathbb{N}}$  be an effective listing of all the linear time computable languages. Fix a set  $A$  in quadratic time but not in linear time. We claim that the  $\Sigma_2^0$  Boolean algebra

$$\mathcal{B} = (\{A \cap L : L \text{ is linear time}\}, \subseteq^*) / =^*$$

with the canonical representation given by  $(G_i)_{i \in \mathbb{N}}$ , is  $\Sigma_2^0$  effectively inseparable. To see this, in Lemma 4.8 let  $U, V$  be any pair of  $\Sigma_2^0$  effectively inseparable sets. If  $x \in U$  then  $A \cap L_x =^* \emptyset$ . If  $x \in V$  then  $A \cap L_x =^* A$ . Now we apply Fact 4.5.

The preorder of  $\mathcal{B}$  on indices for linear time computable languages is  $\Sigma_2^0$ -complete by Theorem 4.6. From  $i$  we can compute an index for  $A \cap G_i$  within our effective listing of the quadratic time sets. Hence we can effectively reduce the preorder on  $\mathcal{B}$  to almost inclusion  $\subseteq^*$  among quadratic time computable languages.  $\square$

In the following,  $\leq_r^p$  will denote a polynomial time reducibility in between polynomial time many-one (m) and Turing (T) reducibility. Clearly this reducibility is  $\Sigma_2^0$  on  $\text{DTIME}(h)$ .

**Theorem 4.10.** *Suppose the function  $h$  is time constructible and dominates all polynomials. Then  $\leq_r^p$  on sets in  $\text{DTIME}(h)$  is  $\Sigma_2^0$ -complete preorder.*

For instance, we can let  $h(n) = 2^n$ , or  $h(n) = n^{\log \log n}$ . The proof relies on the following notion of Ambos-Spies [2].

**Definition 4.11.** Let  $f : \mathbb{N} \rightarrow \mathbb{N}$  be a strictly increasing, time constructible function. We say that a language  $A \subseteq \{0\}^*$  is *super sparse* via  $f$  if  $A \subseteq \{0^{f(k)} : k \in \mathbb{N}\}$  and “ $0^{f(k)} \in A$  ?” can be determined in time  $O(f(k+1))$ .

Supersparse sets exist in the time classes we are interested in here.

**Lemma 4.12** ([2]). *Suppose that  $h : \mathbb{N} \rightarrow \mathbb{N}$  is an increasing time constructible function with  $P \subset \text{DTIME}(h)$ , so that  $h(n) \geq n+1$  and  $h$  eventually dominates all polynomials. Then there is a super sparse language  $A \in \text{DTIME}(h) - P$ .*

*Sketch of Proof.* Let  $f(n) = h^{(n)}(0)$ . Since  $h$  eventually dominates all polynomials, we can construct  $A \subseteq \{0^{f(k)} : k \in \mathbb{N}\}$  such that  $A \in \text{DTIME}(h)$ , but still diagonalize against all polynomial time machines.  $\square$

Given a reducibility  $\leq_r^p$ , we write  $\mathbf{a} = \deg_r^p(A)$  for the degree of  $A$ . We write  $[\mathbf{0}, \mathbf{a}]$  for the initial segment of the degrees below  $\mathbf{a}$ , viewed as a partial order.

*Proof of Theorem 4.10.* Recall the e.i.  $\Sigma_2^0$  Boolean algebra  $\mathcal{B}$  from the proof of Theorem 4.9. It is represented via an effective listing  $(G_i)$  of linear time computable languages. We will embed a  $\Sigma_2^0$  quotient of  $\mathcal{B}$  into  $[\mathbf{0}, \mathbf{a}]$ .

Since  $A$  is super sparse, by [20, Lemma 6.2.9] for polynomial time sets  $U, V$  we have

$$A \cap U \leq_r^p A \cap V \Leftrightarrow A \cap (U - V) \in P.$$

So we may well-define a map  $\Phi : \mathcal{B} \rightarrow [\mathbf{0}, \mathbf{a}]$  as follows. For a linear time set  $L = G_i$ , let

$$\Phi(A \cap L / =^*) = \deg_r^p(A \cap L).$$

Let  $\mathcal{I}$  be the  $\Sigma_2^0$  ideal of  $\mathcal{B}$  consisting of all the equivalence classes  $A \cap L / =^*$  such that  $A \cap L \in \mathcal{P}$ . Then  $\mathcal{C} = \mathcal{B} / \mathcal{I}$  is in a natural way a  $\Sigma_2^0$  Boolean algebra, and  $\mathcal{C}$  is effectively embedded into  $[0, \mathbf{a}]$ . Since  $\mathcal{B}$  is e.i. and the map  $\Phi$  is effective, it follows that  $\mathcal{C}$  is e.i. Hence by Theorem 4.6, the  $\Sigma_2^0$  preorder  $\{\langle e, i \rangle : A \cap G_e \leq_r^p A \cap G_i\}$  is complete. Via a computable transformation of indices, this preorder is effectively reducible to  $\leq_r^p$  on  $\text{DTIME}(h)$ .  $\square$

#### 4.4. Almost inclusion of r.e. sets.

**Theorem 4.13.** *The preordering  $\{\langle e, i \rangle : W_e \subseteq^* W_i\}$  of almost inclusion among r.e. sets is  $\Sigma_3^0$ -complete.*

*Proof.* All sets in this proof will be r.e. Fix a non-recursive set  $A$ . By  $X \sqcup Y = A$  we denote that r.e. sets  $X, Y$  are a splitting of  $A$ . That is,  $X \cap Y = \emptyset$  and  $X \cup Y = A$ . We also write  $X \sqsubseteq A$  to denote that  $X$  is part of a splitting of  $A$ .

The following technical tool parallels Lemma 4.8.

**Lemma 4.14.** *Given a disjoint pair of  $\Sigma_3^0$  sets  $U, V$ , we can effectively in  $x$  determine an r.e. splitting  $A = E_x \sqcup F_x$  such that*

$$x \in U \rightarrow E_x \text{ is computable, and } x \in V \rightarrow F_x \text{ is computable.}$$

*Proof of lemma.* We may choose a uniformly r.e. double sequence  $(P_{x,n})_{x,n \in \mathbb{N}}$  of initial segments of  $\mathbb{N}$  such that

$$x \in U \leftrightarrow \exists n [P_{x,2n} = \mathbb{N}] \text{ and } x \in V \leftrightarrow \exists n [P_{x,2n+1} = \mathbb{N}].$$

We fix  $x$ . Let  $g(k, s)$  be the greatest  $t \leq s$  such that  $t = 0$ , or  $P_{x,k,t-1} \neq P_{x,k,t}$ . We enumerate sets  $E_x, F_x$  with  $E_x \sqcup F_x = A$  as follows. At stage  $s > 0$ , if  $a \in A_s - A_{s-1}$ , let  $k$  be least such that  $a < g(k, s)$ . If  $k$  is even, enumerate  $a$  into  $F_x$ ; otherwise, or if there is no such  $k$ , enumerate  $a$  into  $E_x$ .

First suppose that  $x \in U$ . Then the least  $k$  such that  $P_{x,k} = \mathbb{N}$  is even. We have  $\lim_s g(k, s) = \infty$ . If  $k > 0$  then also  $r = \max_{i < k} \lim_s g(i, s) < \infty$ . For any  $v \geq r$ , we have  $v \in E_x \leftrightarrow v \in E_{x,s}$ , where  $s$  is least such that  $g(k, s) > v$ . Therefore  $E_x$  is computable.

If  $x \in V$ , then the least  $k$  such that  $P_{x,k} = \mathbb{N}$  is odd. A similar argument shows that  $F_x$  is computable.  $\square$

Since  $A$  is non-recursive, there is a major subset  $D \subset_m A$  (see [23, Section X.4]). Thus, if  $R \subseteq A$  is computable then  $R \subseteq^* D$ . We now define an e.i.  $\Sigma_3^0$  Boolean algebra  $\mathcal{B}$  which is a subalgebra of the complemented elements in  $[D, A] / =^*$ . Suppose we are given a pair of e.i.  $\Sigma_3^0$  sets  $U, V$ . Let  $(E_x), (F_x)$  be the u.r.e. sequences obtained through the foregoing lemma. We let  $\mathcal{B}$  be the Boolean algebra generated by all the elements of the form  $(E_x \cup D)^*$  for  $x \in \mathbb{N}$ . Since uniformly in  $x$  we can recursively enumerate both  $E_x$  and its complement  $F_x = A - E_x$ , it is clear that  $\mathcal{B}$  is a  $\Sigma_3^0$  Boolean algebra. Furthermore, by Lemma 4.14 and Fact 4.5,  $\mathcal{B}$  is effectively inseparable. So by Theorem 4.6 the preorder of  $\mathcal{B}$ , namely,  $\{\langle x, y \rangle : E_x \subseteq^* E_y \cup D\}$ , is  $\Sigma_3^0$ -complete. Since the sequence  $(E_x)_{x \in \mathbb{N}}$  is uniformly r.e., this establishes the theorem.  $\square$

Note that we have in fact shown that almost inclusion of sets of the form  $(W_e \cup D) \cap A$  is  $\Sigma_3^0$ -complete. It is worth noting that by a result of Maass and Stob [17], the lattice  $[D, A]/=^*$  is unique up to effective isomorphism whenever  $D$  is a major subset of  $A$ . Thus, only the complemented elements of the Maass-Stob lattice, which already in itself is a tiny part of the lattice of r.e. sets under almost inclusion, are needed to establish the completeness of  $\subseteq^*$  as a  $\Sigma_3^0$  preorder.

**Corollary 4.15.** *The equivalence relation  $\{\langle e, i \rangle : W_e =^* W_i\}$  is  $\Sigma_3^0$ -complete.*

*Proof.* This follows immediately from Fact 2.1.  $\square$

The relation  $=^*$  on r.e. indices is the same relation as that called  $E_0^{ce}$  in [8]. In the spirit of that article, we consider this and several other equivalence relations carried over from the Borel theory. The relations  $E_0$ ,  $E_1$ ,  $E_2$ , and  $E_3$  are all standard Borel equivalence relations, whose definitions may be gleaned from the definitions given shortly. If  $A \subseteq \omega$ , we write  $(A)_n = \{x : \langle n, x \rangle \in A\}$  for the  $n$ -th column of  $A$ . The analogues of these for c.e. indices are relations on the natural numbers, derived by letting each natural number  $e$  represent the set  $W_e$ . Thus, for an equivalence relation  $E$  on reals, we let  $i E^{ce} j$  iff  $W_i E W_j$ , yielding the following specific relations.

$$\begin{aligned} i E_0^{ce} j &\iff |W_i \Delta W_j| < \infty & (\iff W_i =^* W_j) \\ i E_1^{ce} j &\iff \forall^\infty n [(W_i)_n = (W_j)_n] \\ i E_2^{ce} j &\iff \sum_{i \in W_i \Delta W_j} \frac{1}{i} < \infty \\ i E_3^{ce} j &\iff \forall n [|(W_i)_n \Delta (W_j)_n| < \infty]. \end{aligned}$$

Now  $E_3^{ce}$  is  $\Pi_4^0$ -complete as a set, and therefore not computably reducible to any of the others, since the others are all  $\Sigma_3^0$ . In the Borel theory,  $E_0 <_B E_1$ ,  $E_0 <_B E_2$ , and  $E_0 <_B E_3$ , with no other reductions holding among these relations. The proofs of the Borel reducibilities can be adapted to give proofs that  $E_0^{ce} \leq_c E_1^{ce}$ ,  $E_0^{ce} \leq_c E_2^{ce}$ , and  $E_0^{ce} \leq_c E_3^{ce}$ . Therefore, Corollary 4.15 gives a further result.

**Corollary 4.16.**  *$E_0^{ce}$ ,  $E_1^{ce}$  and  $E_2^{ce}$  are all  $\Sigma_3^0$ -complete equivalence relations, and hence are bireducible with each other under  $\leq_c$ .*

In contrast, Corollary 3.8 showed that the relation  $E_3^{ce}$  cannot be complete under  $\leq_c$  at any level of the arithmetical hierarchy, as it is  $\Pi_4^0$  but not  $\Sigma_4^0$  by virtue of its  $\Pi_4^0$ -completeness as a set of pairs.

**4.5. Weak truth-table reducibility of r.e. sets.** It is well-known that weak truth-table reducibility  $\leq_{\text{wtt}}$  on r.e. sets is a  $\Sigma_3^0$  preorder with computable supremum operation. It determines the distributive upper semilattice of r.e. weak truth-table degrees; see e.g. [21].

**Theorem 4.17.** *The preorder  $\{\langle e, i \rangle : W_e \leq_{\text{wtt}} W_i\}$  is  $\Sigma_3^0$ -complete.*

*Proof.* Ambos-Spies [1] called an r.e. set  $A$  *antimitotic* if for any r.e. splitting  $A = X \sqcup Y$ , the sets  $X$  and  $Y$  induce a minimal pair in the Turing degrees. He

built an incomputable antimitotic set  $A$ , which we will fix in the following. Let  $\mathbf{a} = \deg_{\text{wtt}}(A)$ .

Note that the degrees  $\{\deg_{\text{wtt}}(X) : X \subseteq A\}$  form a Boolean algebra of complemented elements in  $[\mathbf{0}, \mathbf{a}]$  with the usual degree ordering. We establish an e.i.  $\Sigma_3^0$  subalgebra  $\mathcal{C}$  of this Boolean algebra. We are given a pair of e.i.  $\Sigma_3^0$  sets  $U, V$ . Let  $(E_x), (F_x)$  be uniformly r.e. sequences as in Lemma 4.14. Thus  $E_x \subseteq A$  for each  $x$ . Let  $\mathcal{C}$  be the Boolean algebra generated by the  $\deg_{\text{wtt}}(E_x)$ ,  $x \in \mathbb{N}$ . Since  $A$  is antimitotic, intersection, union and complementation for splits of  $A$  correspond to the operations of meet, join, and complementation in  $[\mathbf{0}, \mathbf{a}]$ . Since the former operations are effective,  $\mathcal{C}$  is indeed a  $\Sigma_3^0$  Boolean algebra.

If  $x \in U$  then  $E_x$  is computable, so  $\deg(E_x) = \mathbf{0}$ . If  $x \in V$  then  $F_x$  is computable, so  $\deg(E_x) = \mathbf{a}$ . This implies that  $\mathcal{C}$  is effectively inseparable. So the preordering of  $\mathcal{C}$ , namely,  $\{\langle x, y \rangle : E_x \leq_{\text{wtt}} E_y\}$ , is  $\Sigma_3^0$ -complete by Theorem 4.6. Since the sequence  $(E_x)_{x \in \mathbb{N}}$  is uniformly r.e., this establishes the theorem.  $\square$

##### 5. TURING REDUCIBILITY ON R.E. SETS IS A $\Sigma_4^0$ -COMPLETE PREORDER

**Theorem 5.1.** *The preorder  $\{\langle i, j \rangle \mid W_i \leq_T W_j\}$  is  $\Sigma_4^0$ -complete. Hence the equivalence relation  $\{\langle i, j \rangle \mid W_i \equiv_T W_j\}$  is also  $\Sigma_4^0$ -complete.*

*Proof.* We fix a  $\Sigma_4^0$  preorder  $R$ . The overall aim of the construction is to build the uniform sequence of c.e. sets  $\{V_i\}_{i \in \omega}$  such that  $iRj$  iff  $V_i \leq_T V_j$ . Following Nies [19] we fix a uniformly c.e. sequence  $X_{e,p}^{i,j}$  of initial segments of  $\omega$ , such that

$$\begin{aligned} iRj &\Rightarrow \text{For almost all } e \text{ and } p, |X_{e,p}^{i,j}| < \infty, \\ \neg iRj &\Rightarrow \forall e \exists p (X_{e,p}^{i,j} = \omega). \end{aligned}$$

The facts “ $X_{e,p}^{i,j} = \omega$ ” and “ $X_{e,p}^{i,j} < \infty$ ” are (uniformly)  $\Pi_2^0$  and  $\Sigma_2^0$  respectively, so each such statement can be measured at a single node on a priority tree measuring infinitary or finitary behaviour. That is,  $\neg iRj$  would be equivalent to the fact that “ $\forall e \exists p$  such that the node measuring  $|X_{e,p}^{i,j}|$  has true infinitary outcome”. Similarly, if  $iRj$  then “for almost all  $e, p$ , the node measuring  $|X_{e,p}^{i,j}|$  has true finitary outcome”. Hence the ordered pairs in  $R$  will determine the true path of the construction, i.e. which nodes are visited infinitely often and which are not. We will arrange the strategies on the construction tree to align our actions with the true path.

In this proof we use  $Y \upharpoonright k$  to mean the first  $k + 1$  bits of  $Y$ . We say “ $Y$  changes below  $k$ ” to mean a change in  $Y \upharpoonright k$ .

**5.1. Requirements and a high-level description.** The requirements to be met are  $S_{i,j}$  for  $i, j \in \omega$ :

$$S_{i,j} : iRj \Leftrightarrow V_i \leq_T V_j.$$

The way in which each  $S_{i,j}$  is satisfied can only be answered by a  $0'''$  oracle, and so it is difficult to split  $S_{i,j}$  explicitly into subrequirements in a meaningful way. We will instead describe the local action of each node on the priority tree and then describe how the priority tree ensures that each requirement  $S_{i,j}$  is met globally.



The construction tree is a labeled binary tree. Each node of length  $\langle i, j, e, p \rangle$  is assigned the set  $X_{e,p}^{i,j}$  and has two outcomes labelled  $\infty, 0$ . As usual  $\infty$  is to the left of 0. If  $\alpha$  is assigned the set  $X_{e,p}^{i,j}$  we write  $i_\alpha, j_\alpha, e_\alpha, p_\alpha, X_\alpha$  for  $i, j, e, p, X_{e,p}^{i,j}$  respectively. Outcome  $\infty$  stands for  $X_\alpha = \omega$ , while outcome 0 stands for  $X_\alpha$  finite.

We now describe the local action of each node  $\alpha$ . During the construction  $\alpha$  has a dual role. During the non-expansionary stages (i.e.  $\alpha * 0$  stages)  $\alpha$  builds a Turing reduction aiming to make  $V_i \leq_T V_j$ . Loosely speaking, this strategy monitors for changes in  $V_i$  and responds with a change in  $V_j$  each time it sees an element entering  $V_i$ . The other (conflicting) strategy of  $\alpha$  acts at each expansionary stage ( $\alpha * \infty$  stage). At each expansionary stage  $\alpha$  checks to see if it can make  $V_i \neq \Phi_{e_\alpha}^{V_j}$  by enumerating some number in  $V_i$  and restraining  $V_j$  (we henceforth we write  $\Phi_\alpha$  instead of  $\Phi_{e_\alpha}$  for convenience). These two strategies are clearly conflicting, and we always initialize the Turing reduction whenever an expansionary stage occurs for  $\alpha$ .

We now describe how  $S_{i,j}$  can be met globally. Suppose  $\neg iRj$ . Then for each  $e$  there is some node  $\alpha$  along the true path with  $i_\alpha = i, j_\alpha = j, e_\alpha = e$  and having true outcome 0. This  $\alpha$  will ensure that  $\Phi_e^{V_j} \neq V_i$ , and so overall we get  $V_i \not\leq_T V_j$ . In this case there could be infinitely many nodes  $\beta$  along the true path also working for  $i, j$ , but which have true outcome  $\infty$ . The local strategy for  $\beta$  will attempt to demonstrate  $V_i \leq_T V_j$ , which cannot possibly be achieved. Indeed each such node  $\beta$  along the true path will be injured infinitely often by another node  $\alpha$  further along the true path aiming to diagonalize  $\Phi_e^{V_j} \neq V_i$ . This is the key difference between this construction and a typical  $\emptyset'''$  tree argument. In the latter case we usually only allow a finite amount of injury to each requirement along the true path, whereas in our case it is necessary that some node gets injured infinitely often along the true path.

Now suppose that  $iRj$ . Then almost every node along the true path working for the pair  $i, j$  must have true outcome 0. In particular there is a shortest node  $\alpha$  along the true path with true outcome 0 (we will later call all possible candidates for  $\alpha$  a *top node*). In this case there could be finitely many top nodes  $\alpha' \subset \alpha$  with true outcome 0. However the Turing reducibilities built by these  $\alpha'$  will all be destroyed (by nodes between  $\alpha'$  and  $\alpha$  which are attempting to diagonalize), and  $V_i \leq_T V_j$  will be demonstrated by the final top node  $\alpha$  along the true path. It is important to note that each top node  $\alpha'$  cannot possibly know anything about the nodes on the true path which are longer than  $\alpha'$ , and so the situation described above cannot be avoided.

We conclude this section with some technical definitions. We say that  $\alpha$  is a top node if there is no  $\beta \subset \alpha$  such that  $i_\alpha = i_\beta$  and  $j_\alpha = j_\beta$ , or if  $\alpha(|\beta|) = \infty$  for the maximal such  $\beta$ . Given a node  $\alpha$  we define the top of  $\alpha$  to be the maximal top node  $\tau \subseteq \alpha$  such that  $i_\alpha = i_\tau$  and  $j_\alpha = j_\tau$ . We say that  $\alpha$  is a child of  $\tau$  if  $\alpha$  has top  $\tau$ .

We let  $Z^\infty(\alpha) = \{\beta \subset \alpha \mid \beta * \infty \subseteq \alpha \text{ and } \beta \text{ is not a top node}\}$ , which are all the nodes extended by  $\alpha$  running the  $\infty$  strategy. Similarly we let  $Z^0(\alpha) = \{\beta \subset \alpha \mid \beta * 0 \subseteq \alpha \text{ and } \beta \text{ is a top node}\}$ .

**5.2. Ensuring  $V_i \leq_T V_j$ .** Each top node  $\alpha$  will build a Turing reduction  $\Delta_\alpha$  to ensure that  $V_{i_\alpha} \leq_T V_{j_\alpha}$  in the case that  $\alpha * 0$  is along the true path. The Turing reduction is built indirectly by specifying the coding markers  $\{\delta_\alpha(x)[s]\}_{x,s \in \omega}$ . Each  $\delta_\alpha(x)[s]$  is not in  $V_{j_\alpha}[s]$  and obey the following standard marker rules: For all  $x$  and  $s$ , we ensure

- (i)  $\delta_\alpha(x)[s] < \delta_\alpha(x+1)[s]$ .
- (ii) If  $\delta_\alpha(x)[s] \neq \delta_\alpha(x)[s+1]$  or if  $\delta_\alpha(x)[s] \downarrow$  and  $\delta_\alpha(x)[s+1] \uparrow$  then  $V_{j_\alpha, s-1} \upharpoonright (\delta_\alpha(x)[s]) \neq V_{j_\alpha, s} \upharpoonright (\delta_\alpha(x)[s])$ .
- (iii) If  $x$  is enumerated in  $V_{i_\alpha}$  at stage  $s$ , and if  $\delta_\alpha(x)[s]$  is defined then  $V_{j_\alpha}$  must change below  $\delta_\alpha(x)[s]$  at the same stage or later.
- (iv)  $\lim_s \delta_\alpha(x)[s]$  exists.

It is easy to see that if these rules are ensured, and if  $\alpha$  is visited infinitely often, then  $V_{i_\alpha} \leq_T V_{j_\alpha}$ : To compute if  $x$  is in  $V_{i_\alpha}$  we find recursively in  $V_{j_\alpha}$  a stage  $s$  where  $V_{j_\alpha}[s]$  is correct up to  $\delta_\alpha(x)[s]$ . This stage exists because of (iv). At all stages  $t > s$  we have  $\delta_\alpha(x)[s] = \delta_\alpha(x)[t]$  because of (ii). By (iii) we must have  $x \in V_{i_\alpha}$  iff  $x \in V_{i_\alpha}[s]$ .

**5.3. Informal description of the strategy.** We now describe the strategy and the method of overcoming the key difficulties. Each top node  $\alpha$  will have a dual role. At  $\alpha * 0$  stages it must extend the Turing reducibility  $\Delta_\alpha^{V_j}$  by picking a fresh value for  $\delta_\alpha(x)$ . It must also ensure the correctness of  $\Delta_\alpha^{V_j}(x)$  by enumerating  $\delta_\alpha(x)$  if  $x$  had been enumerated into  $V_i$  recently. At each  $\alpha * \infty$  stage  $\alpha$  will attempt to diagonalize by enumerating (if possible) some number  $l$  into  $V_i$  and preserving  $V_j$ . Clearly these two strategies are conflicting and we give the diagonalization strategy higher priority; Hence at each  $\alpha * \infty$  stage we reset  $\Delta_\alpha$  and all  $\delta_\alpha$  markers. If  $\alpha * 0$  is along the true path then we ensure  $V_i = \Delta_\alpha^{V_j}$ , and if  $\alpha * \infty$  is along the true path then we ensure  $\Phi_\alpha^{V_j} \neq V_i$ . In the latter case the reduction  $\Delta_\alpha$  fails. Analyzing the overall outcome of the construction we note that if  $\neg iRj$  then for every  $e$  we ensure  $\Phi_e^{V_j} \neq V_i$  at some node along the true path, and if  $iRj$  then there is a final top node  $\tau$  along the true path where  $V_i = \Delta_\tau^{V_j}$  is successfully maintained. In this case there can be finitely many top nodes  $\alpha$  where  $\alpha * \infty \subseteq \tau$  for which  $\Delta_\alpha^{V_j}$  is initialized infinitely often.

We now describe the main issue which will arise in implementing the above strategy. Suppose  $\alpha \subset \beta$  are both working for the same  $i, j$ , and  $\alpha$  has true outcome 0, while  $\beta$  has true outcome  $\infty$ . We may as well assume that  $\alpha$  is the top of  $\beta$ . Now  $\alpha$  and  $\beta$  will have conflicting actions. At each  $\beta * \infty$  stage, we may want  $\beta$  to diagonalize by putting some number  $l$  in  $V_i$ . If  $\delta_\alpha(l)$  has already been defined then  $\alpha$  will later want to correct  $\Delta_\alpha$  by putting  $\delta_\alpha(l)$  into  $V_j$ , which can destroy the diagonalization previously obtained by  $\beta$ . We overcome this by delaying  $\beta$  from diagonalizing. In the meantime, at each  $\beta * \infty$  stage we lift  $\delta_\alpha(k)$  for some fixed number  $k$  by enumerating  $\delta_\alpha(k)$  into  $V_j$  and later picking a fresh value for  $\delta_\alpha$ . Since there are infinitely many  $\beta * \infty$  stages,  $\delta_\alpha(k)$  goes to infinity. Hence  $\beta$  destroys  $\Delta_\alpha$  but will now be able to meet  $\Phi_\beta^{V_j} \neq V_i$  (via diagonalization or an undefined computation).  $\Delta_\alpha^{V_j}$  is now destroyed. However this is fine because  $\beta$  witnesses the fact that  $\alpha$  is not the final top node along the true path. In this case the reduction

$\Delta^{V_j} = V_i$  will be built further down the true path (if  $iRj$ ) or may not be built at all (if  $\neg iRj$ ).

In the general situation (when considering nodes devoted to other pairs  $i', j'$ ) we consider the nodes  $\beta_0, \beta_1 \subset \alpha$  where  $\beta_0, \beta_1$  have true outcome 0,  $\alpha$  has true outcome  $\infty$ ,  $i = i_\alpha = i_{\beta_1}$ ,  $j_{\beta_1} = i_{\beta_0}$  and  $j_{\beta_0} = j_\alpha = j$ . Again if  $\alpha$  wants to diagonalize at some  $\alpha * \infty$  stage it must be careful; A number  $l$  enumerated in  $V_i$  might cause  $\beta_1$  to later correct  $\Delta_{\beta_1}^{V_{j_{\beta_1}}}$  by changing  $V_{j_{\beta_1}}$ , which might in turn cause  $\beta_0$  to correct  $\Delta_{\beta_0}^{V_j}$  by changing  $V_j$ , destroying the previous diagonalization attempt of  $\alpha$ . In this case we observe that along the true path, we must have one of the following three situations hold:

- (i) There is some child  $\gamma_0$  of  $\beta_0$  where  $\gamma_0 * \infty$  is along the true path.
- (ii) There is some child  $\gamma_1$  of  $\beta_1$  where  $\gamma_1 * \infty$  is along the true path.
- (iii) Almost every  $\gamma$  working for the same pair  $\langle i, j \rangle$  as  $\alpha$  along the true path has true 0 outcome.

One of these three situations must hold since  $R$  is transitive. If (iii) holds then it is fine for  $\alpha$  to fail in diagonalizing even though it has true outcome  $\infty$ . If (i) or (ii) holds then  $\gamma_v$  (for some  $v < 2$ ) witnesses that  $\beta_v$  is not the final top node along the true path; In this case  $\gamma_v$  will destroy  $\Delta_{\beta_v}$  by pushing some  $\delta_{\beta_v}(k)$  to infinity. In this case we delay  $\alpha$  from diagonalizing until we find that  $\Phi_\alpha^{V_j}(l)$  has converged on a number  $l$  with use  $u$  safely. That is, the combined response of  $\beta_0$  and  $\beta_1$  to the entry of  $l$  into  $V_i$  will not change  $V_j$  below  $u$ .

**5.4. Markers for capricious destruction.** Each node  $\alpha$  which is not a top node is given a parameter  $k_\alpha$ . At each  $\alpha$ -expansionary stage  $\alpha$  will enumerate  $\delta_\tau(k_\alpha)$  into  $V_{j_\tau}$  and lift this marker. Hence if  $\alpha * \infty$  is along the true path then  $\delta_\tau(k_\alpha)$  goes to infinity. This is fine because  $\tau$  is not the maximal top node along the true path; in this case there may be a top node further down the true path, or there may be none along the true path, and so the Turing reducibility witnessing  $V_{i_\tau} \leq_T V_{j_\tau}$  need not be built at  $\tau$ . The important thing here is to ensure that for each top node  $\tau$  where  $\tau * 0$  is along the true path and  $\tau$  is not the maximal top node along the true path, we have to destroy the  $\tau$  markers and create an interval  $(i, \infty)$  where every point is eventually cleared of  $\tau$  markers. This allows a conflicting diagonalization strategy of lower priority to succeed.

If  $\tau$  is the final top node along the true path then each  $\tau$  marker is lifted finitely often this way, and so the Turing reducibility witnessing  $V_{i_\tau} \leq_T V_{j_\tau}$  will be built at  $\tau$ .

**5.5. Believable computations.** A computation  $\Phi_\alpha^{V_{j_\alpha}}(l)[s]$  with use  $u$  is said to be  $\alpha$ -believable at stage  $s$  if the following two conditions are met.

- For every  $\beta \in Z^0(\alpha)$  there is no  $\beta$  marker below  $u$  which is pending. A  $\beta$  marker  $\delta_\beta(x)$  is said to be pending at  $t$  if  $x$  was enumerated into  $V_{i_\beta}$  at some  $s < t$  where  $\delta_\beta(x)[s]$  is defined and  $V_{j_\beta}$  has not changed below  $\delta_\beta(x)[s]$ .
- There does not exist a sequence of distinct nodes  $\beta_0, \dots, \beta_n \in Z^0(\alpha)$  such that  $i_{\beta_n} = i_\alpha$ ,  $j_{\beta_0} = j_\alpha$ , and for every  $m < n$ ,  $j_{\beta_{m+1}} = i_{\beta_m}$ , and  $\delta_{\beta_m}(l)[s] \downarrow \leq u$ .

The first item says that a computation is  $\alpha$ -believable if there is no  $\delta$  marker below the use that will be enumerated by some  $\beta \subset \alpha$  due to coding. The second item ensures that if  $l$  were to be enumerated by  $\alpha$  into  $V_{i_\alpha}$  for the sake of diagonalization then the resulting sequence of correction actions will not cause the computation to be later destroyed.

**5.6. Formal construction.** At stage 0 we initialize every node. This means to reset  $\delta_\alpha$  and  $k_\alpha$  to be undefined. At stage  $s > 0$  we define  $\delta_s$  of length  $s$ , where  $\delta_s$  is the stage  $s$  approximation to the true path of construction. Assume that  $\alpha \subset \delta_s$  has been defined. If  $|X_\alpha|$  has increased since the last  $\alpha$ -stage then we play outcome  $\infty$  for  $\alpha$ , otherwise play outcome 0 for  $\alpha$ . Initialize every node to the right of  $\alpha * o$  where  $o$  is the outcome played. We now act for  $\alpha$ . There are four cases.

- (i)  $\alpha$  is not a top node and outcome 0 is played. Do nothing.
- (ii)  $\alpha$  is a top node and outcome  $\infty$  is played. Initialize  $\alpha$ . Check if it is possible to diagonalize, and if so, do it. This means to check if there exists some  $l < s$  such that  $\Phi_\alpha^{V_{j_\alpha}}(l)[s] \downarrow$  is  $\alpha$ -believable,  $l \in \omega^{[\alpha]} - V_{i_\alpha}[s]$ , and for every  $l' \leq l$  we have  $V_{i_\alpha}(l') = \Phi_\alpha^{V_{j_\alpha}}(l')[s]$ . We also require  $l$  to be larger than the previous stage where  $\alpha$  was initialized by another node. If such  $l$  is found enumerate  $l$  into  $V_{i_\alpha}$  and initialize every node extending  $\alpha$ .
- (iii)  $\alpha$  is not a top node and outcome  $\infty$  is played. Let  $\tau$  be the top of  $\alpha$ . If  $k_\alpha \uparrow$  we pick a fresh value for it. Otherwise if  $\delta_\tau(k_\alpha) \downarrow$  enumerate it into  $V_{j_\tau}$  and make it (and all larger  $\tau$  marker) undefined. Check if it is possible to diagonalize, and if so, do it.
- (iv)  $\alpha$  is a top node and outcome 0 is played. We will correct and extend the Turing reduction  $\Delta_\alpha$ . For correction, we check to see if there is any number  $l$  enumerated into  $V_{i_\alpha}$  since the last visit to  $\alpha$ . Let  $l$  be the least. If  $\delta_\alpha(l) \downarrow$  we enumerate it in  $V_{j_\alpha}$  and make  $\delta_\alpha(l) \uparrow$ . To extend  $\Delta_\alpha$  we let  $k < s$  be the least such that  $\delta_\alpha(k) \uparrow$ . We pick a fresh value  $x \in \omega^{[\alpha]}$  and set  $\delta_\alpha(k) \downarrow = x$ .

**5.7. Verification.** Let  $TP = \liminf_s \delta_s$  be the true path of the construction, which clearly exists as the construction tree is finitely branching. We first prove the following key lemma.

**Lemma 5.2.** *Suppose  $\alpha$  is a node on the true path. Then  $\alpha$  makes finitely many diagonalization attempts.*

*Proof.* Assume we are at a stage of the construction where each  $\beta \subset \alpha$  makes no more diagonalization attempt, and we never move left of  $\alpha$ . Suppose  $\alpha$  makes infinitely many diagonalization attempts after this. Let  $l$  be the smallest number put in by  $\alpha$  due to a diagonalization attempt, at some stage  $t$ . At  $t$  we have  $\Phi_\alpha^{V_{j_\alpha}}(l)[t] \downarrow = 0$  with use  $u$ , and is an  $\alpha$ -believable computation. We claim that at  $t$ ,  $V_{j_\alpha}$  is stable below  $u$ , which contradicts the assumption that infinitely many attempts are made.

By convention  $u < t$ , and so the only nodes which can enumerate a number less than  $u$  into any set after stage  $t$ , are nodes of the form  $\beta \subseteq \alpha$ . In fact we cannot have  $\beta = \alpha$  because if  $\alpha$  is a top node then it is initialized at  $t$  and any further diagonalization action by  $\alpha$  involves a number  $l' > l$ , and if

$\alpha$  is not a top node then  $k_\alpha$  or  $\delta_\tau(k_\alpha)$  is undefined before the diagonalization at  $t$ . Similarly we cannot have  $\beta * \infty \subseteq \alpha$ , because if  $\beta$  is a top node then it is initialized at  $t$  and never again performs diagonalization, and if  $\beta$  is not a top node then  $k_\beta$  or  $\delta_\tau(k_\beta)$  is undefined when  $\alpha$  was visited at  $t$ . Hence if  $\beta$  is a node which enumerates a number smaller than  $u$  into any set after  $t$ , it must satisfy  $\beta_0 \in Z^0(\alpha)$ .

Let  $\beta_0 \in Z^0(\alpha)$  be the first node after  $t$  to change  $V_{j_\alpha}$  below  $u$ , say at  $t_0 > t$ . At  $t_0$ ,  $\beta_0$  is correcting  $\Delta_{\beta_0}$  and responding to the action of  $\beta_1$  at  $t_1$ ,  $t_1 < t_0$ . Let  $l_0$  be the number which was enumerated by  $\beta_1$  into  $W_{i_{\beta_0}}$  at  $t_1$ . We cannot have  $t_1 < t$  because of  $\alpha$ -believability at  $t$ . Suppose  $t_1 > t$ . Since  $l < \delta(l)$  for every  $l, \delta$ , we have  $\beta_1 \in Z^0(\alpha)$ . Then we have correction at  $t_1$  and so  $j_{\beta_1} = i_{\beta_0}$ . We can then repeat to get a sequence  $\beta_0, \dots, \beta_n \in Z^0(\alpha)$  and  $t_0 > \dots > t_n > t_{n+1} = t$ , where  $\beta_{n+1} = \alpha$ ,  $j_{\beta_0} = j_\alpha$ ,  $i_{\beta_n} = i_\alpha$ , and for every  $m < n$ ,  $j_{\beta_{m+1}} = i_{\beta_m}$ . Furthermore for every  $m \leq n$ ,  $l_m < \delta_{\beta_m}(l_m) = l_{m-1}$ , and  $l_n = l$ . Since  $\delta_{\beta_m}(l_m)[t_m] \downarrow < u$  we have  $\delta_{\beta_m}(l_m)[t] \downarrow < u$  (since  $\delta$  markers are picked fresh), and so  $\delta_{\beta_m}(l)[t] \downarrow < u$ . By removing any cycles in the sequence, we may assume that the nodes  $\beta_0, \dots, \beta_n$  are distinct. This contradicts the second requirement for  $\alpha$ -believability at  $t$ .  $\square$

We devote the remainder of the proof to showing that each requirement  $S_{i,j}$  is met. Fix  $i$  and  $j$  such that  $iRj$ . Let  $\alpha$  be the shortest node along  $TP$  such that  $i_\alpha = i$ ,  $j_\alpha = j$  and for every node  $\beta$  such that  $i_\beta = i$  and  $j_\beta = j$  where  $\alpha \subseteq \beta \subset TP$ , we have  $\beta * 0 \subseteq TP$ . Clearly  $\alpha$  is a top node. We argue that  $V_i = \Delta_\alpha^{V_j}$ . By symmetry we have  $V_i \equiv_T V_j$ . Since  $\alpha$  has true 0 outcome there are finitely many self-initializations, and by Lemma 5.2, there are only finitely many initializations to  $\alpha$ . The marker rules (i), (ii) and (iii) are clearly met for  $\delta_\alpha$ . We argue (iv) holds. Since  $\alpha$  is visited infinitely often it suffices to check that each marker is made undefined finitely often. Each  $\delta_\alpha(k)$  can only be made undefined by some child of  $\alpha$ , and only when  $k = k_\alpha$ . A child of  $\alpha$  to the left of the true path only does this finitely often, while a child to the right of the true path has  $k_\alpha = k$  at finitely many stages. A child on the true path must have true 0 outcome and acts at only finitely many stages.

Now fix  $i$  and  $j$  such that  $\neg iRj$ . Fix an  $e$ , and let  $\alpha$  be the node on the true path such that  $i_\alpha = i$ ,  $j_\alpha = j$ ,  $e_\alpha = e$  and  $\alpha * \infty \subset TP$ . We argue that  $\Phi_\alpha^{V_j} \neq V_i$ . Suppose that  $\Phi_\alpha^{V_j} = V_i$ .

**Lemma 5.3.** *For almost every  $l$ , the true computation  $\Phi_\alpha^{V_j}(l)$  is eventually  $\alpha$ -believable.*

*Proof.* Call a sequence of distinct nodes  $\beta_0, \dots, \beta_n \in Z^0(\alpha)$  such that  $i_{\beta_n} = i_\alpha$ ,  $j_{\beta_0} = j_\alpha$ , and for every  $m < n$ ,  $j_{\beta_{m+1}} = i_{\beta_m}$ , a bad sequence. There are only finitely many bad sequences, and for each bad sequence there exists a node  $\gamma$  which is a child of some  $\beta_m$  such that  $\gamma * \infty \subset TP$ . This follows by the transitivity of  $R$ . Since  $\gamma$  is not a top node it is initialized finitely often and so achieves a stable value for  $k_\gamma$ . We consider  $l$  larger than every  $k_\gamma$  and argue that  $\Phi_\alpha^{V_j}(l)$  is eventually  $\alpha$ -believable.

Fix  $l$  and fix a large stage  $s$  such that  $\alpha$  is visited and  $\Phi_\alpha^{V_j}(l)[s] \downarrow$  with the correct use  $u$ . The first requirement for  $\alpha$ -believability must be met

because any pending marker from any node in  $Z^0(\alpha)$  must be enumerated in  $V_j$  below  $u$  before the next visit to  $\alpha$ . For the second requirement, we can assume that  $s$  is large enough such that for each bad sequence and associated  $\gamma$ , we have  $\delta_{\beta_m}(k_\gamma)[s] > u$ . This is possible because at each visit to  $\gamma * \infty$  we make  $\delta_{\beta_m}(k_\gamma)$  undefined.

Let us assume the second requirement for  $\alpha$ -believability fails. Fix a bad sequence witnessing this, and let  $\gamma$  be the associated node. By the failure of believability we have  $\delta_{\beta_m}(l)[s] \leq u$ ; On the other hand by the choice of  $s$  we have  $\delta_{\beta_m}(l)[s] > \delta_{\beta_m}(k_\gamma)[s] > u$ , a contradiction.  $\square$

By Lemma 5.2, there are only finitely many initializations to  $\alpha$  initiated by a different node; Let  $s$  be large enough so that there are no more initializations to  $\alpha$  of this type. By Lemma 5.3 pick  $l$  large enough, and wait for  $\Phi_\alpha^{V_j}(l)$  to become  $\alpha$ -believable, and for  $\Phi_\alpha^{V_j}$  to agree with  $V_i$  below  $l$ . Hence  $\alpha$  will make another diagonalization attempt after  $s$ . Since this holds for any large  $s$ , we have a contradiction to Lemma 5.2. This ends the proof of Theorem 5.1.  $\square$

We also obtain as a corollary, examples of complete equivalence relations at the  $\Sigma_{n+4}^0$  level for each  $n \in \omega$ :

**Corollary 5.4.** *For each  $n \in \omega$ , the preorder  $\{\langle i, j \rangle \mid W_i^{\emptyset^{(n)}} \leq_T W_j^{\emptyset^{(n)}}\}$  is  $\Sigma_{n+4}^0$ -complete.*

*Proof.* The proof of Theorem 5.1 produces a computable function  $f$  such that for each  $i, j$ , we have  $iRj$  iff  $W_{f(i)} \leq_T W_{f(j)}$ . Relativizing this to  $\emptyset^{(n)}$ , for each  $\Sigma_{n+4}^0$  preorder  $R$ , we get a function  $f \leq \emptyset^{(n)}$  such that  $iRj$  iff  $W_{f(i)}^{\emptyset^{(n)}} \leq_T W_{f(j)}^{\emptyset^{(n)}} \oplus \emptyset^{(n)}$ .

For each oracle  $X$  and each function  $g \leq_T X$  there is a computable function  $h$  such that  $W_{g(i)}^X = W_{h(i)}^X$  for every  $i$ . To see this, observe that the set  $\{(n, i) : n \in W_{g(i)}^X\}$  is  $\Sigma_1^{0, X}$  and is hence equal to the domain of  $\Psi^X$  for some functional  $\Psi$ . The function  $h$  is obtained by applying the s-m-n Theorem.

Now applying this fact we may assume that  $f$  is computable. Let  $\hat{f}$  be a computable function such that  $W_{f(i)}^X \oplus X = W_{\hat{f}(i)}^X$  for all  $X$  and  $i$ . Then  $\hat{f}$  witnesses that  $R$  is reducible to the preorder  $\{\langle i, j \rangle \mid W_i^{\emptyset^{(n)}} \leq_T W_j^{\emptyset^{(n)}}\}$ .  $\square$

**Corollary 5.5.** *For each  $n \in \omega$ , the preorder  $\{\langle i, j \rangle \mid W_i^{(n)} \leq_T W_j^{(n)}\}$  is  $\Sigma_{n+4}^0$ -complete.*

*Proof.* The proof of Corollary 5.4 shows that the preorder  $\{\langle i, j \rangle \mid W_i^{\emptyset^{(n)}} \oplus \emptyset^{(n)} \leq_T W_j^{\emptyset^{(n)}} \oplus \emptyset^{(n)}\}$  is complete among the  $\Sigma_{n+4}^0$  preorders. By the uniformity of Sacks' Jump Inversion Theorem (see [23, Corollary VIII.3.6.]), for each  $n$  there is a computable function  $q_n$  such that for all  $x$ , we have  $W_{q_n(x)}^{(n)} \equiv_T W_x^{\emptyset^{(n)}} \oplus \emptyset^{(n)}$ .  $\square$

**Corollary 5.6.** *For each  $n \in \omega$  the relation*

$$\{\langle i, j \rangle : W_i^{\emptyset^{(n+1)}} \equiv_1 W_j^{\emptyset^{(n+1)}}\}$$

is  $\Sigma_{n+4}^0$ -complete.

## 6. OPEN QUESTIONS

Many effective equivalence relations from algebra can be considered under the aspect of relative complexity. For instance:

**Question 6.1.** *Is the equivalence relation  $E$  of isomorphism between finite presentations of groups  $\Sigma_1^0$ -complete?*

Adyan and Rabin independently showed in 1958 that the triviality problem, whether a finite presentation describes the trivial group, is  $m$ -complete. See Lyndon and Schupp [16, IV.4.1], letting the given group  $H$  there have a word problem  $m$ -equivalent to the halting problem. In fact, by the discussion following the proof there, every single equivalence class of  $E$  is  $m$ -complete: being isomorphic to a particular finitely presented group  $P$  is incompatible with free products in their sense because  $P * A$  has higher rank than  $P$  for any nontrivial group  $A$ .

**Question 6.2.** *Is the equivalence relation  $E$  of isomorphism between automatic equivalence relations  $\Pi_1^0$ -complete?*

Kuske, Liu and Lohrey [15] showed that this is  $\Pi_1^0$ -complete as a set of pairs. A similar question can be asked about the  $\Pi_1^0$  relation of elementary equivalence of automatic structures for the same finite signature.

By [9], isomorphism on many natural classes of computable structures is a  $\Sigma_1^1$ -complete equivalence relation.

**Question 6.3.** *Is isomorphism of computable Boolean algebras a  $\Sigma_1^1$ -complete equivalence relation?*

Isomorphism of *computable* of computable Boolean algebras is  $\Sigma_3^0$  complete by [11] .

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